

## onTAP Supported Netlists

We have ability to make and add netlist translators and models for your project. Contact us at [Support@flynn.com](mailto:Support@flynn.com) and let us know your netlist format.

Please note that fields without comments offer excerpts which may be found in onTAP's Help section under *Netlists Supported*.

Netlist Format	File Extensions	Comments
Aug	.AUG	%net and %part sections. Each net name, reference designator, and part type is delimited by backslash
GenRad	.CKT	For ATE at Board Test
GenCad	.NET	
EDIF	.EDN or.EDF	
Allegro Format	.NET	ViewLogic source. Characterized by \$PACKAGES and \$NETS expressions
BOARD STATION	.NET + .PKG	Net and package files must have the same root name
Cadstar	.NET + .FRS	
Cadstar RINF Output	.FRS	
Cadence (Concept)	.DAT	
Cadence PST	.DAT	
CadSoft (EAGLE versions)	NETS.ASC + Material.ASC	
FabMaster	NETS.ASC + Material.ASC	
Intel Rep netlist files	Net_rep.txt + Cpn_rep.txt	
Isis	.SDF	
IPL Wirelist	.WL	%net and %part sections. Each net name, reference designator, and part type is delimited by backslash
ViewLogic Generic Format	.NET + .PKG	
PADS POWERPCB	.ASC or .NET	Characterized by *PART* and *SIGNAL* expressions
orCAD	.NET	Check for OrCAD PCB II Netlist Format text at the top of the file.
OrCAD PCB II Format	Net	Characterized by expressions such as "{compins","(net",{node".
PCad	.NET	
PC Easy	.NET	
Protel	.NET	
Protel2	.NET	
Agilent's BOARD		For ATE at Board Test
Teradyne's IPL.DAT	.DAT	For ATE at Board Test
Teradyne's Victory	.CDS	
Zuken CR5000	Multiple .NET	
Zuken Visula	.REP + .LSR	.REP file includes netlist and .LST file includes component list.